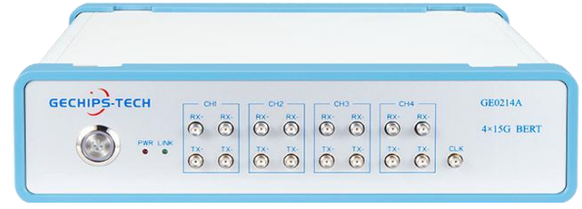
✧ GE0214A 4*10G Bit Error Ratio Tester

GE0228A BERT is a high-performance,low-cost,easy-to-operate,and feature-rich 4-channel parallel 15G high-speed code stream generator and analysis device.

➢ Performance characteristics:

- ❖ Four-channel synchronous output, with each channel outputting 15G. It is used for testing 40G products.Bit error testing can be carried out independently for each channel or synchronously for four channels.
- ❖ Supports continuous bit error detection and timing bit error monitoring.
- ❖ Supports Rate Range:622M~15G,Max reach:15G.
- ❖ Supports any rate, ranging from 50M to15G.

The receiving lock is at least 350M,and bit error analysis is realized.



- ❖ Test patterns: PRBS7,PRBS9,PRBS15,PRBS23,PRBS31,PRBS58,User Pattern(64-bit user-defined code pattern).
- ❖ Communication method: USB port communication. Communicate with the device through PC upper computer software,set the bit error tester and test the bit error rate,with driver-free HID.It supports WINXP/WIN7/WIN10 32/64-bit systems.

➢ Technical Parameter

No.	Parameter	Minimum	Typical	Maximum	Unit
1	Data Rate	0.622	622M,1.244G,1.25G,2.488G,4.25G,5G,6.25G,8.5G,9.953G,10G,10.3125G,11.7G,etc. For CPRI: 0.614M,1.228G,2.457G,3.072G,4.915G,6.144G,9.830G,10.317G; Fibre Channel: 2.125G, 4.25G, 8.5G, 10G, 14.025GThe highest supported rate is 15G	15	Gbps
3	Output Clock Division Optional		Division ratios: 2, 4, 8, 16, 32, 64		
4	Code Pattern		PRBS7, PRBS9, PRBS15, PRBS23, PRBS31, PRBS58, 64-bit user-defined		
5	Rise/Fall Time (20% to 80% / 10.3125G)		30	40	ps
6	Pattern Inversion		Supports inversion of transmitted and received signals		
7	Signal Output Amplitude (Differential)	200	800	1000	mv
8	Data Output Jitter		2 (10.3125G below)		
9	Data Input/Output Interface	SMA			
10	Data Signal Input Sensitivity (Differential)		0.1		Vpp
11	Data Output Eye Diagram Cross-Point		50	54	%
12	Operating Voltage (DC)	5	5	5.5	V

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